L	Hits	Search Text	DB	Time stamp
Number				
1	236	zero near3 power near3 down	USPAT;	2003/08/27
		-	US-PGPUB;	18:05
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
2	204	zero near3 power near4 standby	USPAT;	2003/08/27
_		-	US-PGPUB;	18:05
			EPO; JPO;	
1			DERWENT;	
			IBM TDB	
3	76	(zero near3 power near4 standby) and	USPAT;	2003/08/27
_		(pmos p-mos p-channel pfet)	US-PGPUB;	18:11
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
4	37	((zero near3 power near4 standby) and	USPAT;	2003/08/27
		(pmos p-mos p-channel pfet)) and leak\$3	US-PGPUB;	18:12
			EPO; JPO;	
			DERWENT;	
			IBM TDB	

L	Hits	Search Text	DB	Time stamp
Number	1	(VO. 6400505 A III 6277050 A	IIGDAM -	2002/00/20
1	Hits 45	(US-6400595-\$ or US-6377859-\$ or US-6375344-\$ or US-6128682-\$ or US-4551718-\$ or US-6445222-\$ or US-4396137-\$ or US-6345180-\$ or US-4984204-\$ or US-6345180-\$ or US-5924081-\$ or US-5687387-\$ or US-5924081-\$ or US-55687387-\$ or US-5579489-\$ or US-5563838-\$ or US-4967108-\$ or US-5563838-\$ or US-540519-\$ or US-5383137-\$ or US-6417580-\$ or US-6289407-\$ or US-6393509-\$ or US-6282668-\$).did. or (US-6073195-\$ or US-6366123-\$ or US-5754869-\$ or US-6366123-\$ or US-6307802-\$ or US-6163206-\$ or US-5638330-\$ or US-5771196-\$ or US-5638330-\$ or US-5570050-\$ or US-5638330-\$ or US-5570050-\$ or US-5182810-\$ or US-5532623-\$ or US-5182810-\$ or US-5021680-\$ or US-4853563-\$ or US-4103190-\$).did. or (US-20010034246-\$ or	USPAT; US-PGPUB; DERWENT	Time stamp 2003/08/28 15:55
2	1	US-20020101257-\$).did. or (AU-680398-\$).did. ((US-6400595-\$ or US-6377859-\$ or US-6375344-\$ or US-6128682-\$ or US-4551718-\$ or US-6445222-\$ or US-4396137-\$ or US-5338978-\$ or US-4984204-\$ or US-6427072-\$ or US-6356057-\$ or US-6345180-\$ or US-5924081-\$ or US-5687387-\$ or US-5924081-\$ or US-5563838-\$ or US-5924081-\$ or US-5563838-\$ or US-4967108-\$ or US-5563838-\$ or US-4967108-\$ or US-5224010-\$ or US-440519-\$ or US-5383137-\$ or US-6417580-\$ or US-6289407-\$ or US-6393509-\$ or US-6282668-\$).did. or (US-6073195-\$ or US-6282668-\$).did. or (US-6078195-\$ or US-6366123-\$ or US-5754869-\$ or US-6366123-\$ or US-5754869-\$ or US-5794137-\$ or US-56307802-\$ or US-6366123-\$ or US-4970689-\$ or US-5771196-\$ or US-5638330-\$ or US-5771196-\$ or US-5565808-\$ or US-5570050-\$ or US-5565808-\$ or US-5532623-\$ or US-182810-\$ or US-5532623-\$ or US-4853563-\$ or US-4103190-\$).did. or (US-20010034246-\$ or US-20020101257-\$).did. or (AU-680398-\$).did.) and ((I/O (input/output)) adj2 pad)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/28 15:56
3	6047	(I/O (input/output)) adj2 pad	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/08/28 15:57
4	311	((I/O (input/output)) adj2 pad) with (power adj2 supply)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/08/28
5	7	<pre>(((I/O (input/output)) adj2 pad) with (power adj2 supply)) with isolat\$3</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/28

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7	46	• • • • • • • • • • • • • • • • • • •	USPAT; US-PGPUB; DERWENT	2003/08/28 16:03
		US-6128682-\$ or US-4551718-\$ or US-6445222-\$ or US-6396137-\$ or US-5338978-\$ or US-4984204-\$ or US-6427072-\$ or US-6356057-\$ or US-6345180-\$ or US-5924081-\$ or US-5687387-\$ or US-5579489-\$ or US-5524010-\$ or US-4967108-\$ or US-55383137-\$ or US-440519-\$ or US-55383137-\$ or US-6417580-\$ or US-4276594-\$ or US-6393509-\$ or US-6282668-\$ or US-6073195-\$).did. or (US-5794137-\$ or US-673195-\$).did. or (US-5794137-\$ or US-6307802-\$ or US-6366123-\$ or US-6307802-\$ or US-6163206-\$ or US-4970689-\$ or US-5771196-\$ or US-5638330-\$ or US-5770050-\$ or US-556808-\$ or US-5532623-\$ or US-5182810-\$ or US-5521680-\$ or US-5914627-\$).did. or (US-20010034246-\$ or US-5914627-\$).did. or (US-20010034246-\$ or US-20020101257-\$).did. or	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	16:04
8	85	(AU-680398-\$).did.) and (tri-stat\$3) ((I/O (input/output)) adj2 pad) with (tri-stat\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/08/28 16:05
9	2	<pre>(((I/O (input/output)) adj2 pad) with (power adj2 supply)) with (tri-stat\$3)</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/08/28 16:05
10	2	5,117,129.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/08/28 17:28
-	3059	(processor CPU microprocessor) and ((power adj2 supply) with bus) and switch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/08/27 18:04

Composessor CPU microprocessor) and (() (() () () () () () () () () () () (us) and US-PGPUB; 00:01	682 1	I -
Switch and memory and (power adj2 EPO; JPO; signal)		1	
Signal	wer adi2 EDO: JDO:		
14			
14	DERWENT;		
14	IBM TDB		
((power adj2 supply) with bus) and switch) and memory) and (power adj2 berwent; IBM TDB (((power adj2 supply) with bus) same ((power adj2 supply) with bus) same ((power adj2 supply) with bus) same ((power adj2 supply) with bus) and ((power adj2 supply) supply)		14	_
Switch and memory and (power adj2 SEPO; JPO; DERWENT; IBM TDB USPAT; (((power adj2 supply) with bus) same ((power adj2 signal)) and switch Sepo; JPO; DERWENT; IBM TDB USPAT; (((((power adj2 signal)) and switch Sepo; JPO; DERWENT; IBM TDB USPAT; (((((power adj2 supply) with bus) and uspation USPAT; ((((((power adj2 supply) with bus) and uspation USPAT; ((((((power adj2 supply) with bus) and uspation USPAT; ((((((power adj2 supply) with bus) and uspation USPAT; ((((((((((((((((((((((((((((((((((((İ
Signal) and (isolation adj circuit) DERWENT; IBM TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT; USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT; US-PGPUB; EPO; JPO; JPO; DERWENT; IBM TDB USPAT; US-PGPUB; EPO; JPO; JPO; DERWENT; IBM TDB			
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(power adj2 signal) and switch (power adj2 supply) with bus) and switch) and memoryy and (power adj2 signal)) and (isolation adj circuit)) and (power adj2 supply) with bus) and (power adj2 supply) supply) supp		0	-
Compared to the compared of	itch EPO; JPO;		
Compared and com	DERWENT;		
Compared and com	IBM TDB		
((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((mos or mosfet) and ((mos or mosfet) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((mos or mosfet or fet or transistor) with switch) - 3 (((((((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((mos or mosfet or fet or transistor) with switch)) and pad ((input output) adj2 pad buffer - 532614 ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply)) - 136 ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply) spar; supply) - 136 ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply) spar; supply)	cessor) and USP $\overline{A}T$: 2002/09/20	3	l <u>-</u>
switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and (DERWENT; IBM_TDB ((((((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((mos or mosfet or fet or transistor) ((((((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((mos or mosfet or fet or transistor) with switch) (((((((processor CPU microprocessor) and ((mos or mosfet or fet or transistor) with switch) (((((((processor CPU microprocessor) and ((power adj2 signal)) and (isolation adj circuit)) and ((power adj2 signal)) and (isolation adj circuit)) and ((power adj2 signal)) and (power adj2 signal)) and (isolation adj circuit)) and ((mos or mosfet or fet or transistor) with switch)) and pad ((mos or mosfet or fet or transistor) with switch)) and pad ((mos or mosfet or fet or transistor) with switch)) and pad (input output) adj2 pad buffer USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US			
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- 12 ((((((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((((((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((mos or mosfet or fet or transistor) ((((((processor CPU microprocessor) and ((mos or mosfet or fet or transistor)) ((((((processor CPU microprocessor) and ((power adj2 supply)) with bus) and switch) (((((processor CPU microprocessor) and ((power adj2 supply)) with bus) and switch) and ((power adj2 supply)) with bus) and ((mos or mosfet or fet or transistor)) ((mos o			
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signal)) and (isolation adj circuit)) and (MOS or mosfet or fet or transistor) ((((((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and (isolation adj circuit)) and ((MOS or mosfet or fet or transistor) with switch) 3 ((((((processor CPU microprocessor) and ((MOS or mosfet or fet or transistor) with switch) ((((((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and (isolation adj circuit)) and ((MOS or mosfet or fet or transistor) signal)) and (isolation adj circuit)) and ((MOS or mosfet or fet or transistor) with switch)) and pad ((input output) adj2 pad buffer 532614 ((input output) adj2 pad buffer) and ((input output) adj2 pad buffer) and ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 Supply)) 136 ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 Supply)) 137 (VINPAT; VINPAT; V			
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switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((MOS or mosfet or fet or transistor) with switch) 3 ((((((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and (isolation adj circuit)) and ((MOS or mosfet or fet or transistor) with switch)) and (isolation adj circuit)) and ((MOS or mosfet or fet or transistor) with switch)) and pad (input output) adj2 pad buffer 532614 (input output) adj2 pad buffer) and ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply)) 136 ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply)) EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;		·	
signal)) and (isolation adj circuit)) and ((MOS or mosfet or fet or transistor) with switch) - 3 (((((((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((MOS or mosfet or fet or transistor) with switch)) and pad (input output) adj2 pad buffer - 532614 ((input output) adj2 pad buffer) and ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply)) - 136 ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply)) - 136 ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply))			
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with switch) (((((((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((MOS or mosfet or fet or transistor) with switch)) and pad (input output) adj2 pad buffer 532614 ((input output) adj2 pad buffer) and ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply)) with switch) USPAT; USPAT; USPGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; U			
- 3 (((((((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((MOS or mosfet or fet or transistor) with switch)) and pad (input output) adj2 pad buffer - 532614 ((input output) adj2 pad buffer) and ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply)) - 136 ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply)) - 136 ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply))	ransistor) IBM_IDB		
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((Mos or mosfet or fet or transistor) with switch)) and pad (input output) adj2 pad buffer USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 US-PGPUB; EPO; JPO; JPO; DERWENT) (internal near3 bus) with (power adj2 US-PGPUB; EPO; JPO; DERWENT) (USPAT; US-PGPUB; EPO; JPO; DERWENT) (USPAT; US-PGPUB; EPO; JPO; DERWENT)			
with switch)) and pad (input output) adj2 pad buffer USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply)) USPAT; US-PGPUB; EPO; JPO; US-PGPUB; EPO; JPO;			
- 532614 (input output) adj2 pad buffer USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US	·		
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB ((internal near3 bus) with (power adj2 US-PGPUB; EPO; JPO; Supply)) 2002/09/23 18:06	fer USPAT: 2002/09/23	532614	_
- 136 ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 US-PGPUB; supply)) EPO; JPO;		552514	
- 136 ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 US-PGPUB; supply)) 2002/09/23 18:06	· ·		
- 136 ((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 Supply)) IBM_TDB USPAT; US-PGPUB; EPO; JPO; 18:06			
- 136 ((input output) adj2 pad buffer) and USPAT; 2002/09/23 ((internal near3 bus) with (power adj2 US-PGPUB; supply)) EPO; JPO;			
((internal hear3 bus) with (power adj2 US-PGPUB; 18:06 supply))			
supply)) EPO; JPO;		136	-
	DERWENT;		
IBM TDB	IBM TDB		
- 122 (((input output) adj2 pad buffer) and USPAT; 2002/09/23	uffer) and USPAT; 2002/09/23	122	-
((internal near3 bus) with (power adj2 US-PGPUB; 18:07			
supply))) and control EPO; JPO;			
DERWENT;			
IBM TDB		1	
		-,	
- 71 ((((input output) adj2 pad buffer) and USPAT; 2002/09/24		'1	-
((internal near3 bus) with (power adj2 US-PGPUB; 13:47			
supply))) and control) and (transistor EPO; JPO;	•		
FET MOSFET) DERWENT;			
IBM_TDB			
- 2477 ((processor CPU microprocessor) and USPAT; 2002/09/24	sor) and USPAT; 2002/09/24	2477	-
((power adj2 supply) with bus) and US-PGPUB; 13:47	us) and US-PGPUB; 13:47		
switch) and memory EPO; JPO;			
DERWENT;			
IBM TDB	· · ·		
- 17 (((((input output) adj2 pad buffer) and USPAT; 2002/09/24	_	17	_
		1 1	
supply))) and control) and (transistor EPO; JPO;			
FET MOSFET)) and (((processor CPU DERWENT;			1
	adj2 supply) IBM TDB		
microprocessor) and ((power adj2 supply) IBM_TDB with bus) and switch) and memory)		1	

	155	/	IICDAM -	2002/00/24
-	152	<pre>(power adj supply adj bus) and (isolat\$3 with (power adj supply))</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/09/24 14:25
-	11	(((power adj supply adj bus) and (isolat\$3 with (power adj supply))) and ((cpu processor microprocessor) with (power adj supply))) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/09/24 14:26
	3	(internal with (power adj supply) with bus) and (isolat\$3 adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/09/26 12:56
_	1	<pre>(internal with (power adj supply) with bus) and ((isolat\$3 adj circuit) same (power adj supply))</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/09/26 12:57
	16	(US-6400595-\$ or US-6377859-\$ or US-6375344-\$ or US-6128682-\$ or US-4551718-\$ or US-6445222-\$ or US-6396137-\$ or US-5924081-\$ or US-5224010-\$ or US-5224010-\$ or US-5383137-\$ or US-4356550-\$ or US-4276594-\$).did.	USPAT	2003/02/21 13:37
_	14	<pre>((power adj supply adj bus) and (isolat\$3 with (power adj supply))) and ((cpu processor microprocessor) with (power adj supply))</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/02/21 13:36
-	16	((US-6400595-\$ or US-6377859-\$ or US-6375344-\$ or US-6128682-\$ or US-4551718-\$ or US-6445222-\$ or US-6396137-\$ or US-5338978-\$ or US-4984204-\$ or US-5924081-\$ or US-5224010-\$ or US-5440519-\$ or US-5563838-\$ or US-5383137-\$ or US-4356550-\$ or US-4276594-\$).did.) and (cell cellular pda laptop portable	USPĀT	2003/02/21 16:04
-	2	computer) ((US-6400595-\$ or US-6377859-\$ or US-6375344-\$ or US-6128682-\$ or US-4551718-\$ or US-6445222-\$ or US-6396137-\$ or US-5338978-\$ or US-4984204-\$ or US-5924081-\$ or US-5224010-\$ or US-5440519-\$ or US-5563838-\$ or US-5383137-\$ or US-4356550-\$ or US-4276594-\$).did.) and (cell cellular pda laptop portable computer) and tri-stat\$3	USPAT	2003/02/21 16:09
	4955	(power adj2 supply) near3 bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/02/26 12:11
_	433033	(cpu microprocessor micro-processor processor) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/26 12:08
-	5637	((cpu microprocessor micro-processor processor) and memory) and tri-stat\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/26 12:09

_	78		USPAT;	2003/02/26
		(((cpu microprocessor micro-processor	US-PGPUB;	12:09
		processor) and memory) and tri-stat\$3)	EPO; JPO;	
			DERWENT;	1
			IBM TDB	
_	464	((power adj2 supply) near3 bus) and	USPĀT;	2003/02/26
1		((isolat\$3 near2 circuit) switch) same	US-PGPUB;	12:13
		(disconnect\$3 interupt\$3)	EPO; JPO;	
•		(disconnect)	DERWENT;	
			IBM TDB	
		(((power adj2 supply) near3 bus) and	USPAT;	2003/02/26
1 -	•	(((cpu microprocessor micro-processor	US-PGPUB;	12:14
		processor and memory and tri-stat\$3))	EPO; JPO;	12.14
	ļ			
	1	and (((power adj2 supply) near3 bus) and	DERWENT;	
		((isolat\$3 near2 circuit) switch) same	IBM_TDB	
	10005	(disconnect\$3 interupt\$3))		2002/02/04
-	12935	(power adj2 supply) with disconnect\$3	USPAT;	2003/03/04
			US-PGPUB;	15:55
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	198		USPAT;	2003/03/04
		and (power adj2 supply adj2 bus)	US-PGPUB;	15:18
	[EPO; JPO;	
	I		DERWENT;	
			IBM_TDB	
-	73	((power adj2 supply) with disconnect\$3)	USPAT;	2003/03/04
1		same (power adj2 supply adj2 bus)	US-PGPUB;	15:18
		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	EPO; JPO;	İ
			DERWENT;	
		,	IBM TDB	
_	17	(((power adj2 supply) with disconnect\$3)	USPAT;	2003/03/04
	'	same (power adj2 supply adj2 bus)) and	US-PGPUB;	15:30
		(microprocessor processor micro-processor	EPO; JPO;	13.30
			DERWENT;	
		cpu)	1	
		111110000000000000000000000000000000000	IBM_TDB	2002/02/04
-	0	(((((power adj2 supply) with	USPAT;	2003/03/04
		disconnect\$3) same (power adj2 supply	US-PGPUB;	15:31
		adj2 bus)) and (microprocessor processor	EPO; JPO;	
		micro-processor cpu)) and (memory ram))	DERWENT;	
	_	and tri-stat\$3	IBM_TDB	
-	8	1112	USPAT;	2003/03/04
		same (power adj2 supply adj2 bus)) and	US-PGPUB;	15:31
		(microprocessor processor micro-processor	EPO; JPO;	
		cpu)) and (memory ram)	DERWENT;	
			IBM_TDB	
-	52	((power adj2 supply) with disconnect\$3)	USPAT;	2003/03/04
	1	and (pda (personaliz\$3 adj2 digital adj2	US-PGPUB;	15:56
	1	assistant))	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	0	(((power adj2 supply) with disconnect\$3)	USPAT;	2003/03/04
	1	same (power adj2 supply adj2 bus)) and	US-PGPUB;	15:56
	1	(((power adj2 supply) with disconnect\$3)	EPO; JPO;	
		and (pda (personaliz\$3 adj2 digital adj2	DERWENT;	
1		assistant)))	IBM TDB	
-	1	1	USPAT;	2003/03/04
		and (power adj2 supply adj2 bus)) and	US-PGPUB;	15:56
		(((power adj2 supply) with disconnect\$3)	EPO; JPO;	
	1	and (pda (personaliz\$3 adj2 digital adj2	DERWENT;	
	1	assistant)))	IBM TDB	
_	270	1	USPAT;	2003/04/18
	2,0	(isolat\$3 near3 (transistor fet mosfet	US-PGPUB;	12:38
	1	(1501ac35 Near5 (cransistor let mosiet \$1fet))	EPO; JPO;	12.30
		4116011		
			DERWENT;	
	150	(/T/O) /input/output))	IBM_TDB	2002/04/10
1 -	130	((I/O) (input/output)) with pad with	USPAT;	2003/04/18
	1	(tristate tri-state)	US-PGPUB;	12:36
			EPO; JPO;	
			DERWENT;	
L		<u> </u>	IBM_TDB	

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-	4	((standby near3 (mode operation)) and	USPAT;	2003/04/18
		(isolat\$3 near3 (transistor fet mosfet	US-PGPUB;	12:35
		\$1fet))) and (((I/O) (input/output))	EPO; JPO;	
		with pad with (tristate tri-state))	DERWENT;	
			IBM_TDB	2002 (24 (12
-	141	((standby near3 (mode operation)) and	USPAT;	2003/04/18
	1	(isolat\$3 near3 (transistor fet mosfet	US-PGPUB;	12:37
		\$1fet))) and (p-channel pfet p-fet p-mos	EPO; JPO;	
		pmos)	DERWENT;	1
			IBM_TDB	0000 (04 (10
-	63		USPAT;	2003/04/18
		((isolat\$3 near3 (transistor fet mosfet	US-PGPUB;	12:45
		\$1fet)) same (p-channel pfet p-fet p-mos	EPO; JPO;	
		pmos))	DERWENT;	
	20	/	IBM_TDB	2003/04/18
-	39	(standby near3 (mode operation)) and	USPAT; US-PGPUB;	12:45
		((isolat\$3 near3 (transistor fet mosfet	EPO; JPO;	12:45
		\$1fet)) with (p-channel pfet p-fet p-mos	DERWENT;	
		pmos))	IBM TDB	
		/standby nagra (mode anomation)) same	USPAT;	2003/04/18
-	8	(standby near3 (mode operation)) same ((isolat\$3 near3 (transistor fet mosfet	USPAT; US-PGPUB;	12:39
		((Isolat\$3 hear3 (transistor let mostet \$1fet)) with (p-channel pfet p-fet p-mos	EPO; JPO;	12.39
			DERWENT;	
1		pmos))	IBM TDB	
	8	/standby near / mode eneration) same	USPAT;	2003/04/18
-	"	(standby near3 (mode operation)) same ((isolat\$3 near3 (transistor fet mosfet	US-PGPUB;	12:46
		\$1fet)) with (p-channel pfet p-fet p-mos	EPO; JPO;	12.40
		pmos))	DERWENT;	
		pillos / /	IBM TDB	
l _	236	zero near3 power near3 down	USPAT;	2003/08/27
	250	Zelo hears power hears down	US-PGPUB;	18:05
			EPO; JPO;	13.33
			DERWENT;	
1			IBM TDB	
_	204	zero near3 power near4 standby	USPAT;	2003/08/27
		loro neare power mour: brancas;	US-PGPUB;	18:05
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	37	((zero near3 power near4 standby) and	USPAT;	2003/08/27
	1	(pmos p-mos p-channel pfet)) and leak\$3	US-PGPUB;	18:12
			EPO; JPO;	
]			DERWENT;	
			IBM TDB	
-	85	(zero near3 power near4 standby) and	USPAT;	2003/08/28
		(pmos p-mos p-channel pfet)	US-PGPUB;	14:08
1			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	20	((zero near3 power near4 standby) and	USPAT;	2003/08/28
		(pmos p-mos p-channel pfet)) and	US-PGPUB;	14:10
		(processor and (power adj2 supply))	EPO; JPO;	
			DERWENT;	
	_		IBM_TDB	0000 (00 (00
-	39	((zero near3 power near4 standby) and	USPAT;	2003/08/28
		(pmos p-mos p-channel pfet)) and ((cpu	US-PGPUB;	14:21
		processor) and (power adj2 supply))	EPO; JPO;	
			DERWENT;	
1	7271	//	IBM_TDB	2002/09/29
-	7371	((cell\$4 adj phone) pda (laptop	USPAT;	2003/08/28
		notebook)) and (cpu processor (micro adj	US-PGPUB;	14:23
		processor)) and (power adj supply)	EPO; JPO;	
			DERWENT;	
I _	5	///golled add phonol add /lanton	IBM_TDB USPAT;	2003/08/28
-	3	(((cell\$4 adj phone) pda (laptop notebook)) and (cpu processor (micro adj	US-PGPUB;	14:24
		processor)) and (cpu processor (micro ad)	EPO; JPO;	1 4 4 4 7
		((isolat\$3 adj2 circuit) with (pmos pfet	DERWENT;	
		p-channel p-mos p-fet))	IBM TDB	
	L	h cuaimer h-mos h-rec	1 1 2011 1 2 2 2	